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Art Unit: 2633

Examiner: David C. Payne

Phone: (703) 306-0004

From: Colin T. Dorrian  
Reg No. 54,658

Re: Application No. 09/784,419; Filed February 15, 2001  
Entitled LINEAR FULL-RATE PHASE DETECTOR AND CLOCK AND  
DATA RECOVERY CIRCUIT

File: 50987/CTD/B600

I HEREBY CERTIFY THAT THIS PAPER IS BEING FACSIMILE TRANSMITTED TO  
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Sheila R. Harter

\*Correspondence: Statement on Reasons for Allowance

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Christie, Parker & Hale, LLP  
350 West Colorado Boulevard  
Post Office Box 7068  
Pasadena, CA 91109-7068  
626-795-9900  
Fax: 626-577-8800

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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Sheila R. Harter

Applicant : Jun Cao  
Application No. : 09/784,419  
Filed : February 15, 2001  
Title : LINEAR FULL-RATE PHASE DETECTOR AND CLOCK  
AND DATA RECOVERY CIRCUIT  
Date of Notice  
of Allowance : May 28, 2004  
Grp./Div. : 2633  
Examiner : David C. Payne  
Docket No. : 50987/RJP/B600

## STATEMENT ON REASONS FOR ALLOWANCE

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

PostOffice Box 7068  
Pasadena, CA 91109-7068  
July 27, 2004

28


Commissioner:

The Applicant believes the Examiner's stated reasons for allowance are unnecessary. The record as a whole makes clear the reasons for allowance and therefore a statement by the Examiner is not necessary or warranted. While the Applicant agrees that the claims are allowable, the Applicant does not acquiesce in the stated reasons for allowance, nor that patentability requires each stated feature as expressed by the Examiner.

Respectfully submitted,

CHRISTIE, PARKER &amp; HALE, LLP

By

  
Colin Dorrian  
Reg. No. 54,658  
626/795-9900

CTD/srh  
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